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# **PATENT APPLICATION**

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Takaaki NODA et al.

Group Art Unit: 1763

Application No.: 10/587,500

Docket No.: 128807

Filed: July 27, 2006

For:

METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE AND

APPARATUS FOR PROCESSING SUBSTRATE

## REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

James A. Oliff

Registration No. 27,075

David R. Kemeny

Registration No. 57,241

JAO:DRK/aha

Date: April 5, 2007

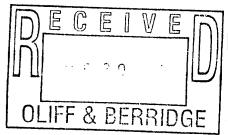
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APPL NO.	FILING OR 371(c) DATE	ART UNIT	FIL FEE REC'D	ATTY.DOCKET NO	TOT CLMS	IND CLMS
10/587,500	07/27/2006	1763	900	128807	17	3

25944 OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320



CONFIRMATION NO. 6657

ζ,

FILING RECEIPT

\*OC000000023118023\*

Date Mailed: 03/27/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Takaaki Noda, Toyama, JAPAN; Kenichi Suzaki, Toyama, JAPAN;

## **Assignment For Published Patent Application**

HITACHI KOKUSALI ELECTRIC INC., <del>TOKYO JAPAN, JAPAN TOKYO JAPAN</del>

Power of Attorney: The patent practitioners associated with Customer Number 25944.

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP05/05699 03/28/2005

#### Foreign Applications

JAPAN 2004-094488 03/29/2004

If Required, Foreign Filing License Granted: 03/26/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/587,500

Projected Publication Date: 07/05/2007

Non-Publication Request: No

Early Publication Request: No

**Title** 

Method of manufacturing semiconductor device and apparatus for processing substrate

**Preliminary Class** 

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